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APPLICATION NO.	I	TLING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/625,843	10/625,843 07/24/2003		Michiru Senda	492322013500	8933
25227	7590	08/09/2005		EXAMINER	
MORRISC 1650 TYSO		ERSTER LLP	PATEL, NITIN		
SUITE 300	110 0001	JE V NGD	ART UNIT	PAPER NUMBER	
MCLEAN,	VA 221	02	2673		
				DATE MAILED: 08/09/2005	

Please find below and/or attached an Office communication concerning this application or proceeding.

	Application No.	Applicant(s)					
	10/625,843	SENDA ET AL.					
Office Action Summary	Examiner	Art Unit					
	Nitin Patel	2673					
The MAILING DATE of this communication ap Period for Reply	pears on the cover sheet with the c	correspondence address					
A SHORTENED STATUTORY PERIOD FOR REPL THE MAILING DATE OF THIS COMMUNICATION. - Extensions of time may be available under the provisions of 37 CFR 1. after SIX (6) MONTHS from the mailing date of this communication. - If the period for reply specified above is less than thirty (30) days, a rep - If NO period for reply is specified above, the maximum statutory period - Failure to reply within the set or extended period for reply will, by statute Any reply received by the Office later than three months after the mailin earned patent term adjustment. See 37 CFR 1.704(b).	136(a). In no event, however, may a reply be tingly within the statutory minimum of thirty (30) day will apply and will expire SIX (6) MONTHS from be, cause the application to become ABANDONE	nely filed s will be considered timely. the mailing date of this communication. D (35 U.S.C. § 133).					
Status							
1) Responsive to communication(s) filed on 10 h	<u>/lay 2005</u> .						
2a) This action is FINAL . 2b) ⊠ This	s action is non-final.						
	Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under <i>Ex parte Quayle</i> , 1935 C.D. 11, 453 O.G. 213.						
Disposition of Claims							
4)⊠ Claim(s) <u>1-10</u> is/are pending in the application							
	4a) Of the above claim(s) is/are withdrawn from consideration.						
	5)⊠ Claim(s) <u>5-10</u> is/are allowed. 6)⊠ Claim(s) <u>1-4</u> is/are rejected. 7.□ Claim(s) is/are objected to						
7) Claim(s) is/are objected to.							
8) Claim(s) are subject to restriction and/o	or election requirement.	•					
Application Papers							
9) The specification is objected to by the Examine	er.						
10) The drawing(s) filed on is/are: a) accepted or b) objected to by the Examiner.							
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).							
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).							
11) The oath or declaration is objected to by the Ex	xaminer. Note the attached Office	Action or form PTO-152.					
Priority under 35 U.S.C. § 119							
12)⊠ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f). a)⊠ All b)□ Some * c)□ None of:							
	1. Certified copies of the priority documents have been received.						
	 2. Certified copies of the priority documents have been received in Application No 3. Copies of the certified copies of the priority documents have been received in this National Stage 						
 Copies of the certified copies of the prio application from the International Burea 		ed in this National Stage					
* See the attached detailed Office action for a list	7 7	d ·					
22 302 2002 2010 2010 101 2110	S Sorumou sopies not receive						
Attachment(s)		•					
1) Notice of References Cited (PTO-892)	4) Interview Summary						
 Property (PTO-948) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) 	Paper No(s)/Mail Da 5) Notice of Informal P	ate atent Application (PTO-152)					
Paper No(s)/Mail Date	6) Other:						

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DETAILED ACTION

Claim Rejections - 35 USC § 112

1. The following is a quotation of the first paragraph of 35 U.S.C. 112:

The specification shall contain a written description of the invention, and of the manner and process of making and using it, in such full, clear, concise, and exact terms as to enable any person skilled in the art to which it pertains, or with which it is most nearly connected, to make and use the same and shall set forth the best mode contemplated by the inventor of carrying out his invention.

Claims 1-4 are rejected under 35 U.S.C. 112, first paragraph, as failing to comply with the enablement requirement. The claim(s) contains subject matter which was not described in the specification in such a way as to enable one skilled in the art to which it pertains, or with which it is most nearly connected, to make and/or use the invention.

In claim 1, Applicant claiming each pixel comprising a serial to parallel converter converting a serial digital image signal supplied serially to the display device to a parallel digital image line and a DA converter converting the parallel digital image to an analog image signal and a pixel electrode configured to receive the analog image signal.

By looking at figure 1,each pixel electrode (19) does not have a serial to parallel converter connected and also a DA converter connected to a single pixel electrode (19 In fig.1). To better understand the whole circuit that a single pixel is connected to a both converters, Examiner suggests a better understanding of claim 1 in well-written and more descriptive forms of claim 1 is necessary to examine claims 1-4.

Allowable Subject Matter

2. Claims 5-10 is allowed.

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3. The prior art fails to teach or suggest a display device having a drain signal line configured to receive a serial digital image serially supplied to the display device; a plurality of pixel selecting transistors connected to the drain signal line and selecting a pixel of the display device; a plurality of shift registers each of the shift registers supplying a sampling pulse to a gate of the corresponding pixel selecting transistor for sampling the serial digital image signal at a predetermined timing to produce a parallel digital image signal; a data retaining portion retaining the parallel digital image signal converted from the serial digital image signal; a pixel electrode of the pixel; a plurality of capacitor electrodes coupled with the pixel electrode, each of the capacitance electrodes having a weighed capacitance ratio to couple with the pixel electrode and a clock supplying portion supplying a periodic clock signal to the capacitor electrodes in response to the parallel digital image signal retained in the data retaining portion as claimed in claim 5.

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Conclusion

4. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Nitin Patel whose telephone number is 571-272-7677. The examiner can normally be reached on 8:00-5:00.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Bipin H. Shalwala can be reached on 571-272-7681. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

NP

July 24, 2005